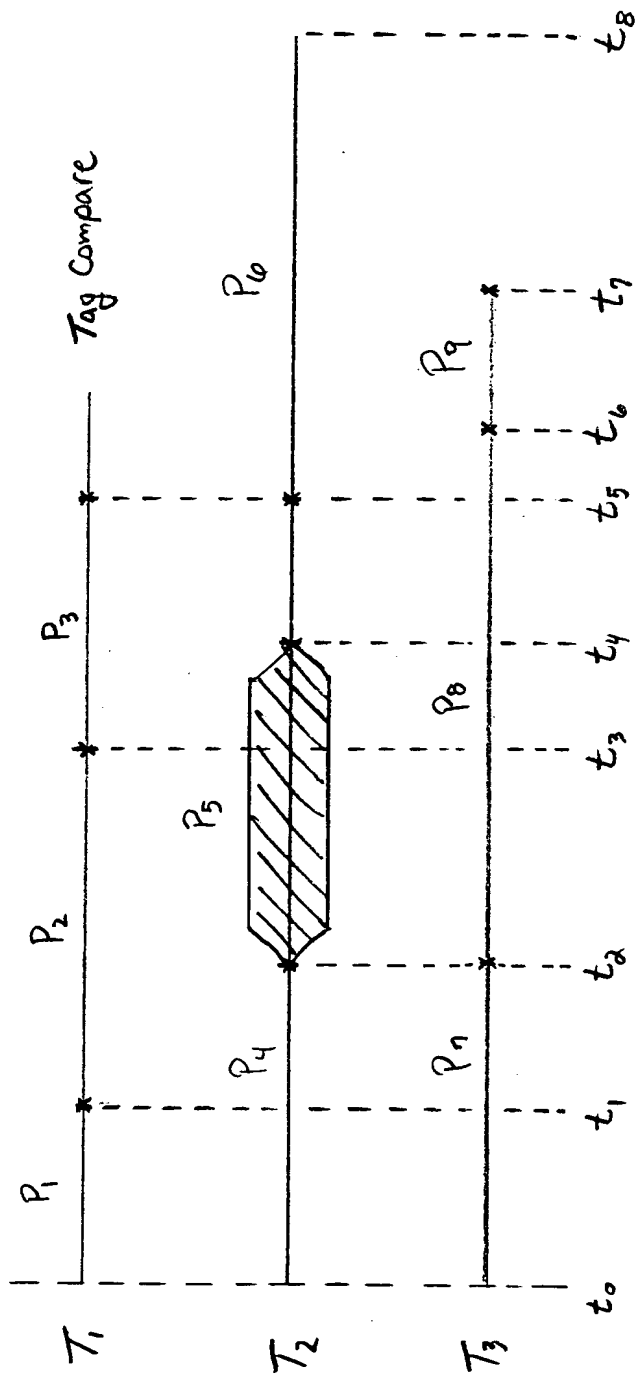


FIG. 2

Parallel Mapping System

$t \rightarrow$



Serial mapper

Parallel mapper

FIG. 3

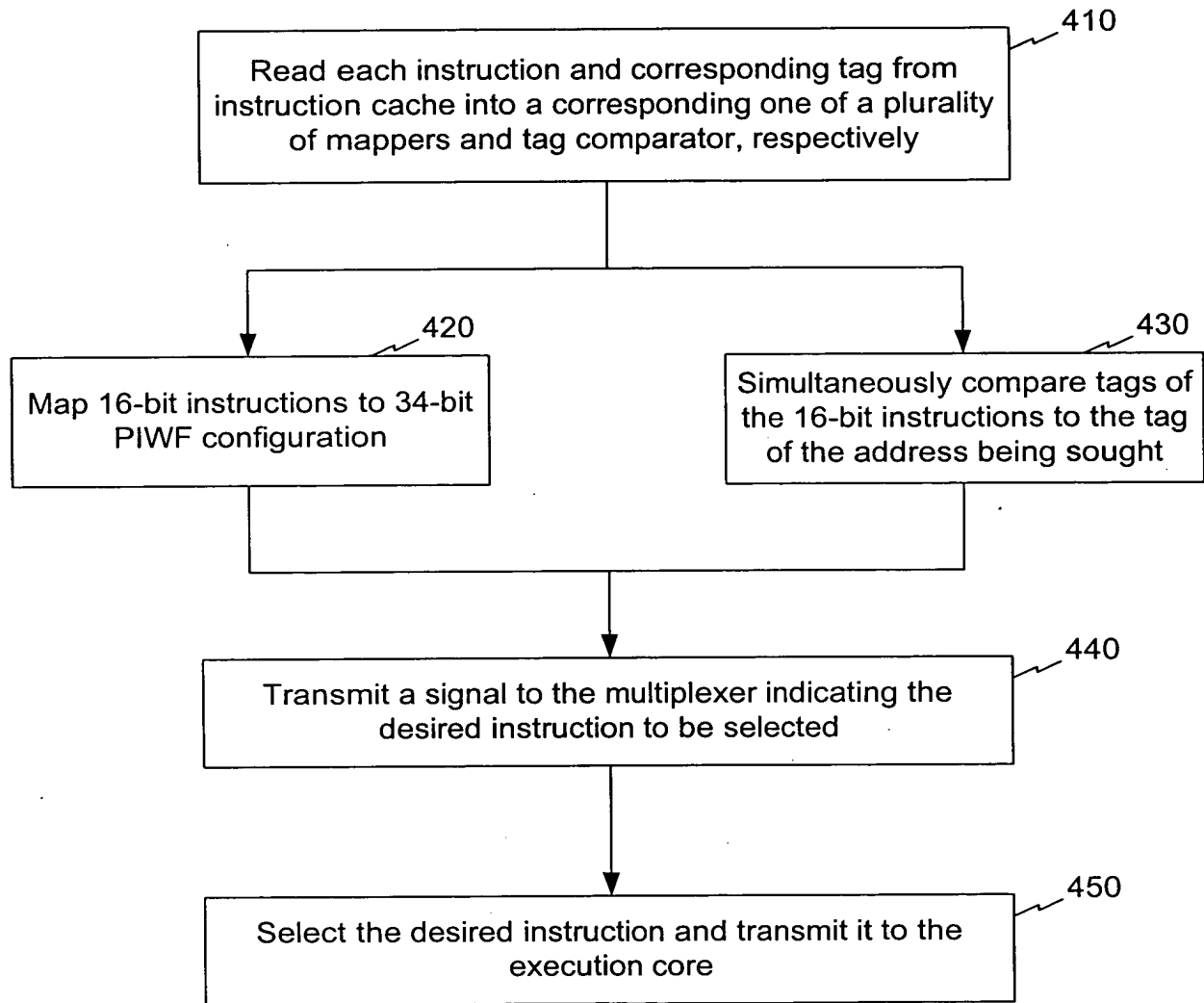


FIG. 4

